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162	23	700/2 and (carrier pcb circuit adj board) and (bus network communicat$4) and (control same regulat$3) and (storage memory) and (input same output i/o) and (cpu microprocessor processor computer controller) and (@ad<20010921 @rlad<20010921)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/08 18:36

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163	2	700/2 and (carrier pcb circuit adj board) and (bus network communicat$4) and (plural$3 multiple more) with ((control same regulat$3) and (storage memory) and (input same output i/o) and (cpu microprocessor processor computer controller)) and (@ad<20010921 @rlad<20010921)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/08 18:39
164	1	700/2 and (carrier pcb circuit adj board) and (bus network communicat$4) and (plural$3 multiple each) with ((control same regulat$3) and (storage memory) and (input same output i/o) and (cpu microprocessor processor computer controller)) and (@ad<20010921 @rlad<20010921)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/08 18:42
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166	2	700/$ and (carrier pcb circuit adj board) and (bus network communicat$4) and (plural$3 multiple each) with ((control same regulat$3) same (storage memory) same (input same output i/o) same (cpu microprocessor processor computer controller)) and (@ad<20010921 @rlad<20010921)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/08 18:44
167	1	700/$ and (bus network communicat$4) and (carrier pcb circuit adj board) same (plural$3 multiple each) with ((control same regulat$3) same (storage memory) same (input same output i/o) same (cpu microprocessor processor computer controller)) and (@ad<20010921 @rlad<20010921)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/08 18:45
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171	91	(bus network communicat$4) and (carrier pcb circuit adj board) same (control regulat$3) same (plural$3 multiple each) with ((storage memory) same (cpu microprocessor processor microcomputer computer controller)) same (input same output i/o) and (@ad<20010921 @rlad<20010921)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/08 18:51

172	59	(bus network communicat$4) same (carrier pcb circuit adj board) same (control regulat$3) same (plural$3 multiple each) with ((storage memory) same (cpu microprocessor processor microcomputer computer controller)) same (input same output i/o) and (@ad<20010921 @rlad<20010921)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/08 18:52
173	0	distribut$4 and vehicle and (bus network communicat$4) same (carrier pcb circuit adj board) same (control regulat$3) same (plural$3 multiple each) with ((storage memory) same (cpu microprocessor processor microcomputer computer controller)) same (input same output i/o) and (@ad<20010921 @rlad<20010921)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/08 18:52
174	35	(distribut$4 vehicle) and (bus network communicat$4) same (carrier pcb circuit adj board) same (control regulat$3) same (plural$3 multiple each) with ((storage memory) same (cpu microprocessor processor microcomputer computer controller)) same (input same output i/o) and (@ad<20010921 @rlad<20010921)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/08 18:54
175	35	(distribut$4 vehicle) and (bus network communicat$4) same (carrier pcb circuit adj board) same (control regulat$3) same (input same output i/o) same (plural$3 multiple each) with ((storage memory) same (cpu microprocessor processor microcomputer computer controller)) and (@ad<20010921 @rlad<20010921)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/08 18:54
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178	0	(distribut$4 and vehicle) same (bus network communicat$4) same (carrier pcb circuit adj board) same (control regulat$3) same (input same output i/o) same (storage memory) same (cpu microprocessor processor microcomputer computer controller) and (@ad<20010921 @rlad<20010921)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/08 18:55
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